

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L21	1	(voltage and driver and (node terminal) and maximum and (high higher) and (low lower) and rail and (second adj (transistor mos mosfet fet \$fet)) and comparator).clm.	US-PGPUB	OR	ON	2007/04/03 16:36
L22	1	(voltage and driver and maximum and (high higher) and (low lower) and rail and (second adj (transistor mos mosfet fet \$fet)) and comparator).clm.	US-PGPUB	OR	ON	2007/04/03 16:40
L23	2	(voltage and driver and maximum and (high higher) and (low lower) and (second adj (transistor mos mosfet fet \$fet)) and comparator).clm.	US-PGPUB	OR	ON	2007/04/03 16:41
L24	18	(voltage and (source supply) and maximum and (high higher) and (low lower) and (second adj (transistor mos mosfet fet \$fet)) and comparator).clm.	US-PGPUB	OR	ON	2007/04/03 16:41

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	((prevent protect) near2 (reverse adj conduct\$4)) and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:10
L2	1	((prevent protect) near2 (reverse adj conduct\$4)) and comparator and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:11
L3	4014	(integrated adj circuit) with (drive driver (power adj (source supply))) and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:12
L4	480	((integrated adj circuit) with (drive driver (power adj (source supply))).ti. and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:12
L6	2	((integrated adj circuit) with (drive driver (power adj (source supply))).ti. and (((high higher) adj voltage) or high-voltage) same (((lowlower) adj voltage) or low-voltage) and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:14
L7	3	((two adj stage) or (dual adj stage) or two-stage or dual-stage) adj driver and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:15
L8	0	((two adj stage) or (dual adj stage) or two-stage or dual-stage) adj driver and (second adj (transistor mosfet fet)) and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:16

EAST Search History

L9	0	(second adj voltage adj rail) with driver with (integrated adj circuit) and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:17
L10	23	307/43.ccls. and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:20
L11	0	307/43,51,75.ccls. and ((reverse adj (current conduct conduct\$4)) with (prevent protect prevent\$4 protect\$4)) and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:21
L12	2	307/51.ccls. and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:22
L13	2	307/71.ccls. and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:23
L14	11	307/80.ccls. and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:25
L15	9	307/85.ccls. and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:26

EAST Search History

L16	3	307/87.ccls. and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:27
L17	1	307/87.ccls. and (transistor mos mosfet) and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:28
L18	0	307/87.ccls. and (transistor mos mosfet) and comparator and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:28
L19	13	361/82.ccls. and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:29
L20	3	361/84.ccls. and @pd>="20060922"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/03 15:29